

Memory Product Specification

DDR2- 533 Unbuffered DIMM

Description

The Module is DDR2-533 CL4 Unbuffered Memory module. The Module density from 512MB to 2GB, it consists 64/128MX8 bit DDR2-533 Synchronous DRAMs in FBGA packages, Memory Module intended for mounting into 240-pin edge connector sockets. The electrical and mechanical specifications are as follows

Features

- JEDEC Standard
- DDR2 Speed Grade : 533Mbps
- Unbuffered DIMM : 240-pin
- Memory Organization : x8 FBGA DRAM chip
- DDR2 DRAM interface : SSTL_18
- CAS latency : 4-4-4
- Bandwidth : 4300MB/s
- VDD voltage : 1.8+/-0.1V
- VDDQ voltage : 1.8+/-0.1V
- Serial presence detect with EEPROM.
- PCB height : **1.18 inch**
- RoHS Compliant
- Application : Desktop